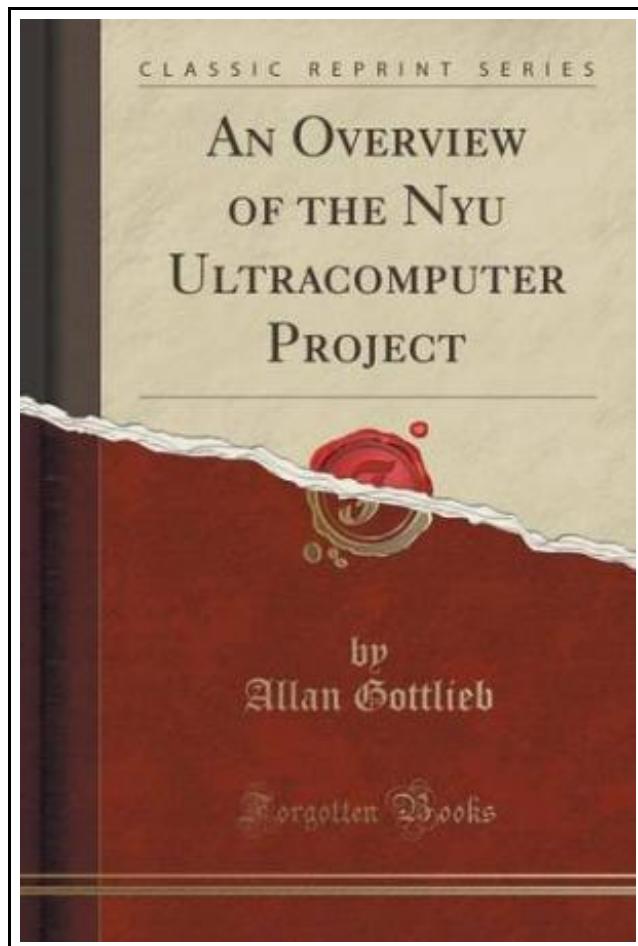


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Forgotten Books, United States, 2015. Paperback. Book Condition: New. 229 x 152 mm. Language: English . Brand New Book ***** Print on Demand *****.Excerpt from An Overview of the Nyu Ultracomputer Project The Nyu Ultracomputer is a shared memory MIMD parallel computer design to contain thousands of processors connected by an Omega network to a like number of memory modules. A new coordination primitive fetch-and-add is defined and the network is enhanced to combine simultaneous requests, including fetch-andadds, directed at the same memory location. The present report, an edited amalgam of papers I have coauthored with other members of the Ultracomputer project, discusses our work on architecture, programming models, system software, hardware and Vlsi design, performance analysis, and simulation studies. 1. Introduction Since its inception in 1979, the Nyu Ultracomputer project has studied the problem of how the fruits of the ongoing microelectronic revolution could be best used to solve problems having computational demands so vast as to make them inaccessible using current technology. Like others, we have always believed that parallelism provides the key new ingredient. With the promise that in 1990 10-20Mips (including floating point instructions) and several megabytes of memory would be available on a handful of chips producing just a few watts, one is naturally led to consider ensembles of thousands of processor with gigabytes of memory Such a configuration would yield several orders of magnitude more performance than do current supercomputers from roughly the same component count. Moreover, the number of distinct components would be quite small and thus the design appears attractive. However, it remains to be demonstrated that this potentially high-performance assembly can be effectively utilized. There are two dimensions to the challenge. First, several thousand processors must be coordinated in such a way that their aggregate power is applied to useful...

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